NET, IAS, State-SET (KSET, WBSET, MPSET, etc.), GATE, CUET, Olympiads etc.: Electronics MCQs (Practice_Test 4 of 13)

1. The action of JFET in its equivalent circuit can be represented as a
   a. Current controlled Current source
   b. Current controlled Voltage source
   c. Voltage controlled Voltage source
   d. Voltage controlled Current source
   - Answer: d
2. A change in the value of the emitter resistance Re in a differential amplifier
   a. affects the difference mode gain Ad
   b. affects the common mode gain Ac
   c. affects both Ad and Ac
   d. does not effect either Ad and Ac
   - Answer: b
3. Generally, the gain of a transistor amplifier falls at high frequencies due to the
   a. Internal Capacitance of the device
   b. Coupling capacitor at the input
   c. Skin effect
   d. Coupling capacitor at the output
   - Answer: a
4. In a common emitter, unbypassed resistor provides
   a. voltage shut feedback
   b. current series feedback
   c. negative voltage feedback
   d. positive current feedback
   - Answer: c
5. A constant current signal across a parallel RLC circuits gives an o/p of 1.4V at the signal frequency of 3.89kHz. At the frequency of 4 kHz, the o/p voltage will be

a. 1 V
b. 2 V
c. 1.4 V
d. 2.8 V

- Answer: b

6. Class AB operation is often used in power (large signal) amplifiers in order to
   a. get maximum effeciency
   b. Remove even harmonics
   c. Overcome a crossover distortion
   d. reducing collector dissipation
   - Answer: c
7. The bandwidth of an RF tuned amplifier is dependent on
   a. Q-factor of the tuned O/P circuit
   b. Q-factor of the tuned I/P circuit
   c. Quiescent operating point
   d. Q-factor of the O/P and I/P circuits as well as quiescent operating point
   - Answer: a
8. Most of the linear ICs are based on the two-transistor differential amplifier because of its
   a. input voltage dependent linear transfer characteristics
   b. high voltage gain
   c. high input resistance
   d. High CMMR
   - Answer: d
9. Negative feedback in an amplifier
   a. Reduces gain
   b. Increase frequency and phase distortion
   c. Reduces bandwidth
   d. Increase Noise
   - Answer: a
10. A dc power supply has no-load voltage of 30V and a full-load voltage of 25V at full-load current of 1A. Its output resistance and load regulation respectively are
    a. 5 ohm & 20%
    b. 2 5 ohm & 20%
    c. 5 ohm &16.7%

d. 25 ohm & 16.7%

- Answer: b

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1 Answer

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